IN THE UNITED STATES PATENT AND TRADEMARK OFFICE Moinul H. Khan et al.

Title:

DYNAMIC TLB LOCKING

Docket No.:

80107.023US1

Filed:

June 24, 2003

Examiner:

Unknown

Serial No.: 10/602509

Due Date: N/A

Group Art Unit: 2186

Commissioner for Patents

P.O.Box 1450

Alexandria, VA 22313-1450

We are transmitting herewith the following attached items (as indicated with an "X"):

A return postcard.

An Information Disclosure Statement (2 pgs.), Form 1449 (1 pg.), and copies of 2 cited documents.

If not provided for in a separate paper filed herewith, Please consider this a PETITION FOR EXTENSION OF TIME for sufficient number of months to enter these papers and please charge any additional fees or credit overpayment to Deposit Account No. 50-2359.

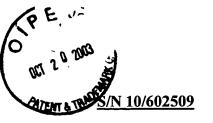
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CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: Commissioner for Patents, P.O.Box 1450, Alexandria, VA 22313-1450, on this 141 day of October, 2003.

Andrew J. Kaul Name





IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: M

Moinul H. Khan et al.

Examiner:

Unknown

2186

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INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the enclosed materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. §1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No 50-2359. in order to have this Information Disclosure Statement considered.

Serial No :10/602509 Dkt: P16760 (INTEL)

Filing Date: June 24, 2003 Title: DYNAMIC TLB LOCKING Assignee: Intel Corporation

The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Respectfully submitted,

MOINUL H. KHAN ET AL.

By their Representatives, LeMoine Patent Services, PLLC c/o PortfolioIP P.O. Box 52050 Minneapolis, MN 55402 952-473-8800

Date October 14, 2003

Dana LeMoine

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nder the Paperwork Reduction Act of 1995, no persons are required to respond to a collection of information unless it contains a valid QMB control nu Substitute for form 1449A/PTO Complete if Known INFORMATION DISCLOSURE STATEMENT BY APPLICANT **Application Number** 10/602509 **Filing Date** June 24, 2003 (Use as many sheets as necessary) **First Named Inventor** Khan, Moinul **Group Art Unit** 2186 Unknown **Examiner Name** Attorney Docket No: 80107.023US1 Sheet 1 of 1

	US PATENT DOCUMENTS										
Examiner Initial *	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Class	Subclass	Filing Date If Appropriate					

FOREIGN PATENT DOCUMENTS								
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	OTHE	R DOCUMENTS NON PATENT LITERATURE DOCUMENTS	
Examiner Initials*	Cite Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue numb publisher, city and/or country where published.		T²
		RAHIN, MOHAMMAD, et al., "Call Admission Control Algorithms in ATM Networks: A Performance Comparison and Research Directions", (September 1998), 26 pages	
		UHLIG, RICHARD, et al., "Design Tradeoffs for Software-Managed TLBs", ACM Transactions on Computer Systems, Volume 12, No. 3,(August 1994), pp. 175-205	